IN THE CLAIMS

Please amend the claims as follows:

1. (original) Circuit arrangement for operating a high pressure discharge lamp comprising

- input terminals for connection to a supply voltage source,
- a DC-DC-converter coupled to the input terminals for generating a DC current out of a supply voltage supplied by the supply voltage source and comprising
- a control loop for controlling the DC current at a value that is represented by a reference signal Sref,
- a control circuit for adjusting the reference signal Sref, and
- an output capacitor,
- a commutator for commutating the DC current and comprising lamp connection terminals,

characterized in that the control circuit comprises means for adjusting the reference signal Sref in dependency of the amplitude of a periodical voltage that is present across the output capacitor and is caused by the commutation of the DC current.

2. (original) Circuit arrangement according to claim 1, wherein the control circuit comprises circuitry for generating a signal Scorr that represents the momentary amplitude of an AC voltage

across the output capacitor and circuitry for subtracting the signal Scorr from the reference signal Sref.

3. (original) Circuit arrangement according to claim 2, wherein the signal Scorr is proportional to the momentary amplitude of the AC voltage across the output capacitor.

4. (original) Circuit arrangement according to claim 3, wherein the circuit arrangement is equipped with circuitry for adjusting the ratio between the signal Scorr and the momentary amplitude of the AC voltage across the output capacitor in dependency of the age of the lamp.

5. (original) Circuit arrangement according to claim 4, wherein the circuit arrangement is equipped with circuitry for adjusting the ratio between the signal Scorr and the momentary amplitude of the AC voltage across the output capacitor in dependency of the lamp voltage.

6. (currently amended) Circuit arrangement according to claim 1~~, 2, 3, 4 or 5~~, wherein the control circuit comprises

- a first circuit part for generating a signal Scorr that represents the peak amplitude of the overshoot voltage across the output capacitor,
- a second circuit part for modulating the reference signal Sref at a modulation frequency that equals the frequency of the commutation of the DC current by subsequently
  - decreasing the reference signal Sref by an amount ΔSref during a first time interval Δt1 that starts a second time interval Δt2 before each commutation of the DC current,
  - maintaining the reference signal at the decreased value during a third time interval Δt3,
  - increasing the reference signal Sref by an amount ΔSref during a fourth time interval Δt4,
- a third circuit part for adjusting at least one parameter chosen from the group formed by ΔSref, Δt1, Δt2, Δt3 and Δt4 so that the amplitude of the signal Scorr is minimal.

7. (original) Circuit arrangement according to claim 6, wherein the third circuit part comprises means for increasing and decreasing the value of the parameter until the amplitude of the signal Scorr is minimal.

8. (currently amended) Circuit arrangement according to claim 6 ~~or 7~~, wherein the third circuit part comprises means for adjusting at least 2 parameters chosen from the group formed by ΔSref, Δt1, Δt2, Δt3 and Δt4 so that the amplitude of the signal Scorr is minimal.

9. (original) Circuit arrangement according to claim 8, wherein the third circuit part comprises means for adjusting the parameters ΔSref, Δt2 and Δt3 so that the amplitude of the signal Scorr is minimal.

10. (currently amended) Circuit arrangement according to ~~claims 6, 7, 8 or 9~~claim 6, wherein the third circuit part comprises a microcontroller.